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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re	Patent Application of:)	
	Min-hwa Chi)) Examiner:	Kang, Donghee կ
Applic	cation No. 09/865,929) Art Unit:	2811
Filed:	May 24, 2001)	() DX
For:	TRANSISTOR AND LOGIC CIRCUIT ON THIN SILICON-ON-INSULATOR WAFERS BASED ON GATE INDUCED LEAKAGE CURRENTS	Vatch &	
Attn: 0	ant Commissioner for Patents Chief Draftsperson ington, D.C. 20231		E.a.
RI	EQUEST FOR APPROVAL OF DRAWIN	CHANGE UNDER	37 C.F.R. § 1.121
Sir:			
	Applicants respectfully request approva	f new drawing for	Fig. 10A for the
above-	-identified application as enclosed.		
	Respectfully subm	ed,	
	BLAKELY, SOKO	OFF, TAYLOR & Z	AFMAN LLP
Dated:	Jan Jarol Little Reg. No. 41,181	Lu	
Sevent Los An	Wilshire Blvd. th Floor ngeles, CA 90025-1026 292-8600		
I hereby ce postage in	ertify that this correspondence is being deposited with the Unit an envelope addressed to the Assistant Commissioner for Pate April 10, 2003 (Date of Deposit)	States Postal Service as first, Washington, D. C. 20231	class mail with sufficient on
(Luci M. Arevale (yper or printer many the other correspondence)		
	(Signature of person mailing correspondence)		

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